

**PDPM Indian Institute of Information Technology,
Design & Manufacturing Jabalpur, Dumna Airport Road, Jabalpur - 482 005**

Date: 08 /08 /2016

Advertisement No.:IIITDM /SMDP-C2SD/2016/02

Candidates of Indian nationality are invited to appear for the **Walk-in test / interview** for project appointments under the following project. Appointment shall be on contractual basis on consolidated pay renewable yearly or up to the duration of the project, whichever is earlier.

Title of the Project	Special Manpower Development Program - Chips to System Design (SMDP-C2SD)	
Funding Agency	Department of Electronics & Information Technology (DeitY), Govt. of India	
Principal Investigator	Prof. P.N Kondekar	
Dept./Centre	Electronics & Communication	
Duration of the Project	17/05/2020	
Post (s)	Consolidated Pay	Minimum Qualifications
Lab-Engineer (1 Post)	Rs. 34,000/- pm	B.E/B. Tech in Electronics and Communication In depth knowledge on CAD tools (Cadence/Synopsys) installation, debugging, System level knowledge, Physical design, DRC, LVS, GDS generation.
Project-Assistant (1 Post)	Rs.20,000/- pm	B.E/B. Tech in Electronics and Communication Knowledge of Operating Systems.
Walk-in test/interview	Time of Interview	Venue
25/08/2016	9:00 am	Conference Hall, PDPM IIITDM, Jabalpur, Dumna Airport road, Jabalpur.

The candidates who fulfill the above qualifications/experience should appear for the written test/interview. Candidates must bring all original certificates, passport size photo and original Identity-Card along with one set of photo-copies for verification of educational qualifications, experience and identity of the candidate. Please contact the undersigned for any query.

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