

PDPM

Indian Institute of Information Technology, Design & Manufacturing Jabalpur

Applications are invited for the Position of one JRF* in a sponsored research project supported by Science and Engineering Research Board (SERB), a statutory body under Department of Science & Technology (DST) New Delhi. The details are as follows:

Title of the Project: “**Time-varying harmonics and interharmonics estimation in real-time for online applications**”

Project duration: Three year

Duration of the post: Up to **Nov. 2016**

Job Description: The project work will involve design, implementation and testing of signal processing algorithms on FPGA/DSP platforms.

Essential Qualification: M. Tech./ B.E. / B. Tech. in Electrical/Electronics.

Desired Qualification: M.Tech. with Minimum 70% marks at the Master's level (for M.Tech candidates) or 80% (CGPA 8.00) at the B.Tech level. Knowledge of signal processing, MATLAB, VHDL/Verilog programming, Xilinx FPGA kits, etc. Preference will be given to candidates having knowledge/experience in the area of the project.

Remuneration: **Rs. 16,000/ Rs. 18,000 consolidated, subject to revision as per the DST, GoI.**

Last date for receipt of applications: **June 22, 2015**

How to Apply: Go to the url <http://goo.gl/forms/BJQhMXGRTD> and **fill up the online datasheet** before June 21, 2015. Following this, send your summary of bio-data/CV/Resume to the email: mail2iiitdmj@gmail.com. Please note that your CV/Resume file should be named in the format "your last name_first name.pdf". Applications will not be entertained if both steps (online data filling and email with CV/Resume) are not completed.

Please note that no TA/DA will be given to the candidate called for interview.

Contact:

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* The Post is purely temporary in nature.