

## **PDPM**

### **Indian Institute of Information Technology, Design & Manufacturing Jabalpur**

Applications are invited for the Position of one JRF\* in a sponsored research project supported by Science and Engineering Research Board (SERB), a statutory body under Department of Science & Technology (DST) New Delhi. The details are as follows:

Title of the Project: “**Time-varying harmonics and interharmonics estimation in real-time for online applications**”

Project duration: Three year

Duration of the post: Up to **Nov. 2016**

**Job Description:** The project work will involve design, implementation and testing of signal processing algorithms on FPGA/DSP platforms.

Essential Qualification: M. Tech./ B.E. / B. Tech. in Electrical/Electronics.

Desired Qualification: M.Tech. with Minimum 70% marks at the Master's level (for M.Tech candidates) or 80% (CGPA 8.00) at the B.Tech level. Knowledge of signal processing, MATLAB, VHDL/Verilog programming, Xilinx FPGA kits, etc. Preference will be given to candidates having knowledge in the area of the project.

**Remuneration: Rs. 16,000 consolidated (first two years) then Rs. 18,000 consolidated (third year)**

Last date for receipt of applications: **June 16, 2014**

Application on plain paper, along with a summary of bio-data (containing academic record, namely degree, year of passing, percentage/CPI, etc.) should be sent to [mail2iiitdmj@gmail.com](mailto:mail2iiitdmj@gmail.com). All applicants are also required to **fill the online datasheet** at the following link before June 16, 2014: <https://docs.google.com/forms/d/17pcyeix3URSq85pa1F8QYx55mabWFzbx8oX8LFUD8Zk/viewform>.

Please note that no TA/DA will be given to the candidate called for interview.

#### **Contact:**

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\* The Post is purely temporary in nature.