



Advertisement for Senior Research Fellow (SRF) in the DST-SERB Sponsored Research Project

Applications are invited for the post of Senior Research Fellow (SRF) in the research project entitled “**Design and Analysis of Multiplierless Multirate Filterbank with Low Complexity**” funded by DST SERB, Government of India, under the supervision of Dr. Anil Kumar, Principal Investigator, Department of Electronics and Communication Engineering, PDPM-IIITDM Jabalpur. The details are given below:

Name of Available Positions: 01

Duration: 36 Months

Fellowship: Rs. 30,000/-

Essential Qualifications: Ph.D/M.Tech in Electronics and Communication Engineering or any other related subject with minimum CPI of 5.5 or 55% marks aggregate in last degree. Candidates having good academic and research background will be given preference.

Desirables: Candidate having knowledge of Signal processing/filter design/Matlab/Verilog and hardware realization in FPGA will be preferred.

Selection Procedure: Written Test (if found desirable by the selection committee)/Interview.

How to Apply

Completely filled application form along with detailed *Biodata* may be sent by e-mail with subject “SRF Application of DST-SERB Project” to anilk@iiitdmj.ac.in .

Please note that no TA/DA will be given to the candidates called for interview.

Last date for receipt of application by e-mail: December 15, 2016.

Contact:

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