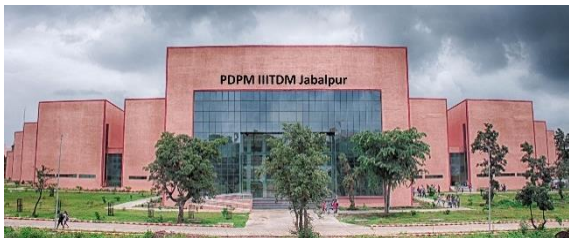


About Electronics & ICT Academy at PDPM IITDM Jabalpur



The Ministry of Electronics and Information Technology (MeitY), Government of India has instituted Electronics and ICT Academies in the year 2015. In the second phase, the academy at PDPM IITDM Jabalpur aims at scalable training programmes in niche areas of Electronics and ICT for the development of the required knowledge base, skills and tools to unleash the talent of the Indian population. The Academy is identified by the MeitY as a hub of activities for capacity building through training, internships, research, and consultancy programmes in fundamental and advanced topics in electronics, information and communication technologies, the Academy conducts customized academic programmes for students, corporate sectors and researchers.

About NITK Surathkal

National Institute of Technology Karnataka (NITK), Surathkal (formerly KREC), is one of the Institutes of National Importance in our country funded by MHRD, the Government of India. Since its inception in 1960 as the Karnataka Regional Engineering College, the Institute has established itself as a premier centre engaged in imparting quality technological education and supporting research and development activities. NITK has carved a niche for itself among the best technical institutes in India and is consistently ranked among the top 17 technological institutions.

Faculty Development Programme (Online Mode: 10 Days)

On

Sustainable Semiconductor Manufacturing and Computational Electronics

The course is designed to provide fundamental and advanced knowledge of recent developments in semiconductor materials, device technologies, sustainable manufacturing processes, and computational approaches for electronic device design and analysis. The primary aim is to equip participants with the knowledge and practical skills required to understand, model, simulate, and optimize semiconductor devices and manufacturing processes using modern computational tools.

Who can attend: Suitable for faculty from colleges, universities, and technical and professional institutes can attend. Students, fresh graduates, researchers, and industry personnel working in allied disciplines can also attend.

Important Dates:

Last Date of Online Registration: 29 Sept. 2026

FDP Dates: October 1 - 10, 2026

Coordinators:

Dr. Dip Prakash Samajdar,

Assistant Professor, ECE, IITDM Jabalpur

Dr. Gopal Rawat,

Assistant Professor, ECE, NITK Surathkal,
Karnataka

Faculty Development Programme (Online Mode: 10 Days)

On

Sustainable Semiconductor Manufacturing and Computational Electronics

1st October – 10th October, 2026

Jointly Organized

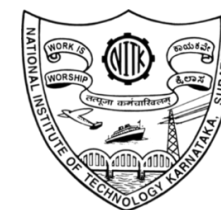
By

**Electronics and ICT Academy IITDM
Jabalpur**



And

**National Institute of Technology Karnataka
(NITK), Surathkal**



*An Initiative of the Ministry of
Electronics and Information Technology,
Government of India*



Faculty Development Programme On **Sustainable Semiconductor Manufacturing and Computational Electronics**

Date: October 1 - 10, 2026 (Online)

Resource Persons

- Mr. H.S. Jattana, Former Scientist G, SCL
- Mr. Deepak Kamath, DGM, SoC, PDIC, BEL
- Mr. Gaurav, Synopsys
- Dr. S K Pandey, NITK Surathkal
- Mr. Kamal Rudra, IBM, USA
- Ms. Shrutika Solat, Keysight
- Dr. Nikhil K S, NITK Surathkal
- Mr. Anand Mukhopadhyay, MATLAB
- Mr. Gaurav, Siemens EDA
- Mr. Deepak Upadhyay, Integrated Microsystem

Coordinators

Dr. Dip Prakash Samajdar,
Assistant Professor, ECE, IIITDM Jabalpur
Email: dip.samajdar@iiitdmj.ac.in

Dr. Gopal Rawat,
Assistant Professor, ECE, NITK Surathkal, Karnataka
Email: gopal.rawat@nitk.edu.in
Website: <https://www.nitk.ac.in/>

Course Contents

- Fundamentals of Computational Electronics
- Emerging Low-Power Semiconductor Technologies
- Advanced Semiconductor Materials Engineering
- Device Modeling and TCAD-Based Simulation
- Semiconductor Fabrication and Manufacturing Processes
- Low-Power VLSI Design and Optimization Techniques
- Nanoelectronics and Advanced Device Architectures
- Sustainable Semiconductor Manufacturing Practices
- AI/ML in Computational Electronics and Semiconductor Design
- Real-World and Industrial Applications of Semiconductor Systems

Hands-On Sessions

- From RTL to Silicon.
- From Fabrication to Device Modeling with a Practical ICCAP Demonstration
- Simulation of VLSI-Nanoscale Devices using Sentaurus TCAD.
- Verification of IP and Subsystem with Simulation and Emulation
- DFT Calculations Using Synopsys QuantumATK.
- Practical session on sensor design and fabrication techniques.
- Accelerating FPGA System Design and Verification using MATLAB and Simulink.
- Data acquisition, signal processing, and analysis exercises.
- Practical session on Advanced Verification and Simulation Technologies for Modern SoCs.

Programme Features

- 10-day expert-led training comprising 22 hours of lectures and 18 hours of hands-on and interactive query sessions.
- Industry-oriented learning through expert lectures and practical sessions.
- Opportunities to connect with experts in the field.
- Certificate on successful completion with full access to the course material.

Registration Details

- Registration link: <https://forms.gle/hDsbLd9FamFe6X5B9>
- Registration fee: ₹ 500/-
- Last Date for Registration: 29 Sept. 2026

Online Payment Details

- **Internet banking**

Beneficiary Name	PDPM IIITDM Jabalpur
Bank Name	INDIAN BANK
A/C No.	50018692852
IFSC Code	IDIB000M694

- **UPI ID: iiitdmj@indianbk**



Contact us:

Mr. Durgesh Kushwaha
Ph. No: 7898670354

academy@iiitdmj.ac.in, eict@iiitdmj.ac.in

Certificates will be issued on successful completion of the course with 80% attendance & Quiz. It is strict mandate of the EICT academy for issuing certificates to the participants.