

Tentative Schedule: Reconfigurable FPGA and Internet of Things, 21 April-02 May, 2025 (Online mode)

Date	Day	10:00-10:30 PM	10:30-12:00 Noon	Break	1:30 PM-2:30PM	2:30-3:30 PM
April 21, 2025	Monday	Inaugural session	Lecture(L1): IoT based Health Monitoring Device: Dr. Varun Bajaj, MANIT Bhopal		L2: Reliability of Core and Datapaths in Advanced CMOS Process : Dr. Bhupendra S. Reniwal Shran, IIT BHU	
April 22, 2025	Tuesday	L3: FPGA-based System Design:Features and Applications Dr. Bal Chand Nagar, NIT Patna			L4: Design and implementation of Multipler-less Filter Bank using FPGA: Dr. Anil Kumar, IIITDM Jabalpur	
April 23, 2025	Wednesday	L5: AI Efficient Edge Hardware Accelerator: Prof. Santosh Kumar Vishwakarma, IIT Indore			L6: Physical Design-Issues & Challenges in Hardware: Dr. Pushpa Raikwal, IIITDM Jabalpur	
April 24, 2025	Thrusday	L7: Hardware Security Primitives for Smart Computing Devices: Dr. Ambika Prasad Shah, IIT Jammu			L8: FPGA-based Approximate Computing for Media Processing: Dr. Bal Chand Nagar, NIT Patna	
April 25, 2025	Friday	L9: Nanometer Devices and their Reliability Issues for Hardware Security Aplications: Dr. Meena Panchore, NIT Patna			L10: FPGA based Brain Inspired Computing: Prof. Jawar Singh, IIT Patna	
April 28, 2025	Monday	LAB1: FPGA Architecture and Vivado: Mr. Hari Prasad, Anmaya Technologies, Udupi Karnataka.			LAB2: Introduction to MICROWIND software and illustration of design techniques: Mr. Shrikant Atkarne,Spaartasoft Technology Solutions, Pune	
April 29, 2025	Tuesday	LAB3: Timing Analysis and Constraints: Mr. Hari Prasad, Anmaya Technologies, Udupi Karnataka.			LAB4: Full custom, Semi-custom and standard cell design of digital circuits: Mr. Shrikant Atkarne,Spaartasoft Technology Solutions, Pune	
April 30, 2025	Wednesday	LAB5: Debugging using ILA: Mr. Hari Prasad, Anmaya Technologies, Udupi Karnataka.			LAB6: Module level design approach: Mr. Shrikant Atkarne,Spaartasoft Technology Solutions, Pune	Quiz
May 1, 2025	Thursday	LAB7: Introduction to Intellectual Property: Mr. Hari Prasad, Anmaya Technologies, Udupi Karnataka.			LAB8: Design technique of Digital IC Design: Mr. Shrikant Atkarne,Spaartasoft Technology Solutions, Pune	
May 2, 2025	Friday	LAB9: Practical design session using schematic and layout: Mr. Shrikant Atkarne,Spaartasoft Technology Solutions, Pune			L11:Cloud Integration to IoT Systems: Dr. Neelam Dayal, IIITDM Jabalpur	Validictory Session