

Online Faculty Development Programme

on

VLSI Design & Nanoelectronics: Emerging Challenges & Opportunities

August 24 – 31, 2022

Organized by:
EICT Academy,
IIITDM Jabalpur

Course Contents

- Semiconductor Fundamentals
- Device Fabrication Techniques
- Nanomaterial synthesis and characterizations
- Device modelling and simulation
- Digital Design / Analog Design
- SoC Design
- High Level Synthesis
- Mixed Signal Design/RF IC Design
- Functional Verification using SV

Key Features

- Online / Live lectures sessions by subject experts.
- Comprehensive tutorials and practice notes.
- Online lab and training sessions

Resource Persons

- Prof. Saurabh Lodha, IIT Bombay
- Prof. Yogesh Chauhan, IIT Kanpur
- Prof. Satyabrata Jit, IIT BHU
- Prof. Gaurav Trivedi, IIT Guwahati
- Prof. Shaibal Mukherjee, IIT Indore
- K. Padmanabhan, Intel Bangalore
- Navaneetha Krishnan, Entuple Technologies

Course Fee Details

Academic (student/faculty): 500 INR

Industry People : 1000 INR

Others : 1000 INR

For Foreign Academics: Rs.4000

Online payment details

Account Name: EICT Academy, IIITDM
Jabalpur

Bank Name: Indian Bank

Account No: 50302042708

IFSC Code: IDIB000M694

Branch Name: Mehgawan, IIITDM Jabalpur

Last Date for registration : August 20, 2022

Registration link:

https://docs.google.com/forms/d/e/1FAIpQLSf_jvPnuWYjla1swR7IkarMjRbtACOXsStmSKzVvo80167RBQ/viewform?vc=0&c=0&w=1&flr=0

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