PDPM Indian Institute of Information Technology, Design and Manufacturing Jabalpur

Department of Electronics and Communication Engineering

Call for Research Internship

Applications are invited from interested and highly motivated candidates for **Training and Skill Internship under VRITIKA, Accelerate Vigyan scheme of Science and Engineering Research Board (SERB)**, **Department of Science and Technology, Government of India**. The internship will be held at IIITDM Jabalpur in physical mode.

Project Title	**Design, Investigation and Development of RF and Microwave Circuits for Modern Communication Technologies**
Sanctioned No.	**AV/VRI/2022/0179**
Objective	The objective of this internship is to provide opportunities to the potential students pursuing their PG program from Universities, Institutes, Research Laboratories and Colleges and would like to accelerate their skills through hands-on experience and training in the domain of RF and Microwave Circuits design.
Position	Research Intern (05)
Internship Duration	04 Weeks (14th November-13th December 2022)
Institute name and address	Indian Institute of Information Technology Design and Manufacturing Jabalpur, Dumna Airport Road Jabalpur M.P.-482005
Event organizer	**Dr. Manoj Singh Parihar** Faculty of Electronics and Communication Engineering, IIITDM Jabalpur Contact: 09425156309, 0761-279-4467 Email: mparihar@iiitdmj.ac.in http://faculty.iiitdmj.ac.in/faculty/mparihar
How to apply	Interested candidates are required to fill the online form using below link and submit latest by **6th November 2022**. https://forms.gle/HmCd8VeSzrneStxo7 Before filling the form, keep ready the below documents in PDF form to upload online:

	1. **NO OBJECTION CERTIFICATE** on Institute's letterhead from Supervisor / Head of the Department / Head of the Institute, indicating their association with the institute, hence allowing their student to undergo internship, if selected. 2. Gate score (if qualified) 3. B.E./B. Tech Grade/Mark sheet 4. M.E./M. Tech Grade/Mark sheet (in case of PhD) 3. Detailed CV highlighting academic credentials and projects done. 4. Statement of Purpose (SOP) of not more than 1page
Eligibility	B.E./B.Tech. with 1st division in Electronics & Communication/ Electronics/ Electrical & Electronics Engineering **and** Pursuing M.E./M.Tech/Ph.D. or equivalent in Electronics & Communication/ Electronics/ Electrical & Electronics Engineering or equivalent allied areas from a recognized University/Institute/College
Desirable	• Good theoretical knowledge of RF & Microwave Circuits (Filters, Couplers, Antennas, Transmission lines) and Wireless Communications. • Good analytical background and experience with simulation tools such as CST, HFSS, IE3D, ADS, MATLAB etc.
Nature of Support and other terms and conditions	• The internship will cover all the daily expenses such as Accommodation, Food, Consumables and Stationery. • The interns will be eligible for Travel Allowance (TA) reimbursement for their journey to the host institute from their hometown / home institute, both ways, as per GoI norms. • The interns will not be given any stipend • Incomplete applications will not be considered for shortlisting. • Only selected candidates will be intimated through email, therefore the candidates should provide correct email ID in their applications. Besides, the list of selected candidates will also be uploaded on Institute's website. • The selected candidates need to acknowledge and accept the internship offer through return email, failing which the waitlisted candidates will be called for the internship. • A certificate shall be issued to the intern by the event organizer and head of the concern department on satisfactory completion of the internship and on submission of the assignment report. • Interns not completing the requisite period will not be issued any certificate. for more details please login to https://acceleratevigyan.gov.in/home and candidates are also advised to create a login account and register as student at Accelerate Vigyan portal of SERB.