PDPM Indian Institute of Information Technology, Design and Manufacturing, Jabalpur

Advertisement for Admission to Full Time PhD Program Under the "Visvesvaraya PhD Scheme for Electronics and IT": Phase-II" Sponsored by MeitY, Govt. of India, for the AY 2024-25

PDPM IIITDM Jabalpur invites applications from highly motivated research-oriented students for admission to the

- Full time Ph.D. Programme - 01 seats

under the "**Visvesvaraya PhD Scheme for Electronics and IT**" in the field of **Electronic System Design and Manufacturing (ESDM) and IT/IT Enabled Services (ITES).** List of research areas is attached as Annexure I.

The salient features of the scheme **for Full Time PhD Programme** are as below:

- Monthly scholarship of Rs. 38,750/- for the first two years and Rs. 43,750/- for the rest 3 years (support up to 5years).
- Rent reimbursement (As per the Government of India norms subject to fulfilment of condition as per the scheme).
- Annual Research Contingency Grant upto Rs.1,20,000/- per year.
- Financial Support upto Rs.1.5 Lakh for attending International Conferences.
- Opportunity to visit labs abroad.

Eligibility for Admissions

A minimum of 60 percent marks OR a CPI/CGPA of 6.0 (on the scale of 10.00) shall be required at the Master's level qualifying examination (M.Tech./ M.E./ MCA) as the specified minimum for admission in a Ph.D. programme for the concerned disciplines. Please see the table below for the eligibility criteria for each discipline.

Ph.D. Program (Discipline)	Minimum Qualifications
Ph.D. in Computer Science and Engineering	M.E./M.Tech. with B.E./B.Tech. /MCA/ M.Sc.in any discipline/branch. *Qualified GATE in Computer Science and Information Technology (CS)* or CSIR/UGC JRF/NET in Computer Science or Information Technology
Ph.D. in Electronics and Communication Engineering	M.E./M.Tech. with B.E./B.Tech./MCA/ M.Sc.in any discipline/branch. *Qualified GATE in Electrical Engineering (EE) or Electronics and Communication Engineering (EC) or Instrumentation (IN) or Computer Science and Information Technology or Physics*
Ph.D. in Smart Manufacturing	M.E./M.Tech. with B.E./B.Tech./MCA/ M.Sc.in any discipline/branch. *Qualified GATE in Electrical Engineering (EE) or Electronics and Communication Engineering (EC) or Instrumentation (IN) or Computer Science and Information Technology (CS) or Physics or Mechanical Engineering (ME) or related field.*

Above eligibility is minimum requirement only. Good academic record and higher qualification will be preferred, and mere eligibility shall not ensure a call for the admission test and/or viva voce.

All interested candidates, fulfilling the minimum eligibility criteria, must submit the application by Google Form and submission of the scanned copy of biodata in the attached format.

Link of Google Form (to be added)

Important Dates

➢	Start date of application	**28.10.2024**
➢	Last date of application submission	**10.11.2024**
➢	Announcement of list of shortlisted candidates	**14.11.2024**
➢	Date of written test and/or interview	**Between 27.11.2024 – 30.11.2024**
➢	Announcement of list of selected candidates	**06.12.2024**

Note:

The institute reserves the right to fix suitable criteria for short listing of eligible candidates satisfying qualification and experience. Only shortlisted candidates may be called for an interview.

For further queries contact at:

Email Address : admission@iiitdmj.ac.in ; aracad@iiitdmj.ac.in or call at 0761-2794036, 6268371310.

Annexure I

List of research areas

- Image Processing and Computer Vision
- Game Theory
- Machine and Deep Learning
- Software Engineering
- Natural Language Processing
- Computer Networks
- Wireless Networks
- Network and Cyber Security
- IoT
- Hardware Security
- Cloud Computing
- Clustering
- Social Network Analysis
- Computational Genomics & Proteomics
- RF and Microwave Engg
- Communication Engineering
- Signal and Image Processing
- Micro and Nano Electronics
- Power and Control System
- **Robotics and Mechatronics**
- IIoT or Industry 4.0